

## PATENT APPLICATION

Sheet 1 of 1

FORM PTO-1449

LIST OF PATENTS AND PUBLICATIONS FOR  
APPLICANT'S INFORMATION DISCLOSURE  
STATEMENT

(Use several sheets if necessary)

ATTY. DOCKET NO.

10018228-4

APPLICATION NO.

10/650,895

CONFIRMATION NO.

8691

APPLICANT

Peter J. Fricke et al

FILING DATE

Aug 25, 2003

GROUP

2824

## REFERENCE DESIGNATION

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
<i>JA</i>	✓1A	1,003,489	May 08, 1963	Weatherald	
<i>JA</i>	✓1B	6,004,825	Dec 21, 1999	Seyyed	
<i>JA</i>	✓1C	4,287,571	Sep 01, 1981	Chakravarti et al	
	1D	<del>5,835,396</del>	<del>Nov 10, 1998</del>	<del>Zhang</del>	repeat
<i>JA</i>	✓1E	6,504,742 B1	Jan 07, 2003	Tran et al	
<i>JA</i>	✓1F	2002/0041525	Apr 11, 2002	Butz	
	1G				
	1H				
	1I				
	1J				
	1K				

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	PUBLICATION DATE	NAME OF PATENTEE OR APPLICANT	Pages/Columns/Lines Where Relevant Passages/Figures Appear	Check if Translation attached
<i>JA</i>	✓1L	EP 0073486	Mar 09, 1983	Toyama		
<i>JA</i>	✓1M	EP 1265287A2	Dec 11, 2002	Hurst et al		
<i>JA</i>	✓1N	EP 0939444A2	Sep 01, 1999	O'Neill		
	1O					
	1P					

## OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

	1Q	
	1R	
	1S	

EXAMINER

*JA*

DATE CONSIDERED

3/23/04



## PATENT APPLICATION

Sheet 1 of 3

PTO FORM PTO-1449

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ATTY. DOCKET NO. <b>10018228-4</b>	APPLICATION NO. <b>10/650,895</b>	CONFIRMATION NO.
APPLICANT <b>Pet r J. Fricke et al</b>		
FILING DATE <b>Aug 25, 2003</b>	GROUP <b>2824</b>	

## REFERENCE DESIGNATION

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
<i>JHA</i>	1A	3,271,591	Sep 06, 1961	Ovshinsky	
<i>JHA</i>	1B	3,530,441	Sep 22, 1970	Ovshinsky	
<i>JHA</i>	1C	3,641,516	Feb 08, 1972	Castrucci et al	
<i>JHA</i>	1D	4,499,557	Feb 12, 1985	Holmberg et al	
<i>JHA</i>	1E	4,599,705	Jul 08, 1986	Holmbert et al	
<i>JHA</i>	1F	5,335,219	Aug 02, 1994	Ovshinsky et al	
<i>JHA</i>	1G	5,625,220	Apr 29, 1997	Liu et al	
<i>JHA</i>	1H	5,659,500	Aug 19, 1997	Mehrad	
<i>JHA</i>	1I	5,751,012	May 12, 1998	Wolstenholme et al	
<i>JHA</i>	1J	5,821,558	Oct 13, 1998	Han et al	
<i>JHA</i>	1K	5,835,396	Nov 10, 1998	Zhang	

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		DOCUMENT NUMBER	PUBLICATION DATE	NAME OF PATENTEE OR APPLICANT	Pages/Columns/Lines Where Relevant Passages/Figures Appear	Check if Translation attached
	1L					
	1M					
	1N					
	1O					
	1P					

## OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

<i>JHA</i>	1Q	Victor, W.C. Chan et al "Multiple Layers of CMOS Integrated Circuits Using Recrystallized Silicon Film" IEEE Electron Device Letters, V. 22(2), Feb. 2001, pp 77-79
<i>JHA</i>	1R	Thomas H. Lee, "A Vertical Leap for Microchips," Scientific American, Jan 2002, pp 53-59
<i>JHA</i>	1S	Esmat Hamdy et al, "Dielectric based antifuses for logic and memory ICs" IEEE International Electron Devices Meeting, IEDM 88, Aug 1988, pp 786-789

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## PATENT APPLICATION

Sheet 2 of 3

FORM PTO-1449

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ATTY. DOCKET NO.

~~10004803-2~~

APPLICATION NO.

~~10/632,721~~

CONFIRMATION NO.

APPLICANT

~~Vladak P. Kasperchik et al.~~

10/650,895

Fricke et al.

FILING DATE

~~Aug 01, 2003~~

GROUP

2824

## REFERENCE DESIGNATION

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
JH	2A	5,942,777	Aug 24, 1999	Chang	
JH	2B	6,002,607	Dec 14, 1999	Dvir	
JH	2C	6,026,017	Feb 15, 2000	Wong et al	
JH	2D	6,034,882	Mar 07, 2000	Johnson et al	
JH	2E	6,111,302	Aug 29, 2000	Zhang et al	
JH	2F	6,185,121B1	Feb 06, 2001	O'Neill	
JH	2G	6,185,122B1	Feb 06, 2001	Johnson et al	
JH	2H	6,033,955	Mar 07, 2000	Kuo et al	
JH	2I	01/0011776A1	Aug 09 2001	Igarashi et al	
JH	2J	01/0055838A1	Dec 27, 2001	Walker et al	
JH	2K	6,351,406B1	Feb 26, 2002	Johnson et al	

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	PUBLICATION DATE	NAME OF PATENTEE OR APPLICANT	Pages/Columns/Lines Where Relevant Passages/Figures Appear	Check if Translation attached
	2L					
	2M					
	2N					
	2O					
	2P					

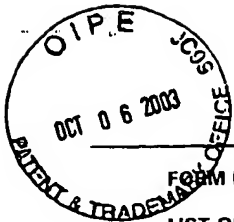
## OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

JH	2Q	Chenming Hu, "Interconnect devices for field programmable gate array." IEEE International Electron Devices Meeting, IEDM 92 (Apr 1992, pp 591-594
JH	2R	Jonathan Green et al, "Antifuse Field Programmable Gate Arrays", Proc IEEE Vol. 81(7), July 1993, pp 1042-1056.
JH	2S	Vivek D. Kulkarni et al, "Patterning of Submicron Metal Features and Pillars in Multil vel Metallization", J. Electrochem. Soc. V.135(12), Dec. 1988, pp 3094-3098. 3

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ATTY. DOCKET NO. <b>10004803-2</b>	APPLICATION NO. <b>10/632,721</b>	CONFIRMATION NO.
APPLICANT <b>Vladek P. Kasperchik et al. Fricke et al.</b>		
FILING DATE <b>Aug 01, 2003</b>	GROUP <b>2824</b>	

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U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
<i>Jef</i>	3A	5,969,380	Oct 1999	Seyyed	
	3B				
	3C				
	3D				
	3E				
	3F				
	3G				
	3H				
	3I				
	3J				
	3K				

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OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

	3Q	
	3R	
	3S	

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*Jef H. H.*

DATE CONSIDERED

*3/23/04*